

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Russell Henry
Max Johnson
Bret Weber
Dennis Gates



Serial No. : 10/004,208

Group Art Unit : 2187

Filed : November 01, 2001

Examiner : Elmore, Reba I.

For : Multiple Memory System Support
Through Segment Assignment

Atty Docket : / 01-219

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

MARCH 7, 2005
Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331

Date: 3/4/05